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Amendment dated October 30, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Hirokazu Hayashi

Group Art Unit: 2128

Serial No.: 10/059,176

Examiner: A. Saxena

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For: METHOD FOR MODELING SEMICONDUCTOR DEVICE PROCESS

AMENDMENT UNDER 37 C.F.R. 1.116

U.S. Patent and Trademark Office
Customer Window, Mail Stop AF
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Date: October 30, 2006

Sir:

In response to the Final Office Action dated August 24, 2006, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.

*Please
Enter
— Aneesh Saxena
10/27/06*